

Claims

1. A display device being characterized in that:

in a pixel region formed on a substrate, a first pixel electrode formed of a light transmissive conductive layer is formed in one optical transmissive region which is formed by partitioning the pixel region and a second pixel electrode formed of a non-light transmissive conductive film is formed on the other light reflective region,

the first pixel electrode is positioned as a lower layer with respect to an insulation film and, a hole is formed in the insulation film in a region corresponding to the light transmissive region so as to expose the first pixel electrode, and the second pixel electrode is formed on a light reflective region of the insulation film, and

at least a portion corresponding to a side wall surface of the hole formed in the insulation film is shielded from light.

2. A display device according to claim 1, wherein the first pixel electrode and the second pixel electrode are formed on one of a pair of substrates which are arranged to face each other in an opposed manner with liquid crystal therebetween and, a light shielding film which is positioned below the insulation film is provided to at least the portion corresponding to the side wall surface of the hole formed in the insulation film.

3. A display device according to claim 1, wherein the first pixel electrode and the second pixel electrode are formed on one of a pair of substrates which are arranged to face each other in an opposed manner with liquid crystal therebetween and, a light shielding film is provided to a portion corresponding to a side wall surface of a hole formed in the insulation film of the other substrate of the respective substrates.

4. A display device being characterized in that:

on one of respective substrates which are arranged to face each other with liquid crystal therebetween, a plurality of juxtaposed gate signal lines and a plurality of juxtaposed drain signal lines which cross the respective gate signal lines are formed,

regions surrounded by these respective signal lines constitute pixel regions, and each pixel region includes a switching element which is operated in response to a scanning signal from the gate signal line and a pixel electrode to which a video signal is supplied from the drain signal line through the switching element,

the pixel electrode is constituted of a first pixel electrode formed of a light transmissive conductive layer which is formed in a light transmissive region which constitutes one region after partitioning the pixel region and a second pixel electrode formed of a non-light transmissive conductive film which is formed in a light reflective region which constitutes

the other region after partitioning the pixel region,

the first pixel electrode is positioned as a lower layer with respect to an insulation film, a hole is formed in the insulation film in a region corresponding to the light transmissive region so as to expose the first pixel electrode, the second pixel electrode is formed in the light reflective region of the insulation film, and

a light shielding film which is positioned as a layer below the insulation film is provided to at least a portion corresponding to a side wall surface of the hole formed in the insulation film.

5. A display device according to the claim 4, wherein the light shielding film is made of a material equal to a material of the gate signal lines.

6. A display device being characterized in that:

on one of respective substrates which are arranged to face each other with liquid crystal therebetween, a plurality of juxtaposed gate signal lines and a plurality of juxtaposed drain signal lines which cross the respective gate signal lines are formed,

regions surrounded by the respective signal lines constitute pixel regions, and each pixel region includes a switching element which is operated in response to a scanning signal from the gate signal line and a pixel electrode to which a video signal is supplied from the drain signal line through

the switching element,

the pixel electrode is constituted of a first pixel electrode formed of a non-light transmissive conductive layer which is formed in a light reflective region formed surrounding a light transmissive region and a second pixel electrode formed of a light transmissive conductive layer which is formed on the light reflective region,

the second pixel electrode is positioned as a lower layer with respect to an insulation film, a hole is formed in the insulation film in a region corresponding to the light transmissive region so as to expose the second pixel electrode, the first pixel electrode is formed in the light reflective region of the insulation film,

a light shielding film which is positioned as a layer below the insulation film is provided to at least a portion corresponding to a side wall surface of the hole formed in the insulation film and,

the light shielding film is formed as a layer below the second pixel electrode and, at the same time, there exists a portion where the light shielding layer is not formed at a part of the portion corresponding to the side wall surface of the hole formed in the insulation film.

7. A display device being characterized in that:

on one of respective substrates which are arranged to face each other with liquid crystal therebetween, a plurality of

juxtaposed gate signal lines and a plurality of juxtaposed drain signal lines which cross the respective gate signal lines are formed,

regions surrounded by these respective signal lines constitute pixel regions, and each pixel region includes a switching element which is operated in response to a scanning signal from the gate signal line and a pixel electrode to which a video signal is supplied from the drain signal line through the switching element,

the pixel electrode is constituted of a first pixel electrode formed of a non-light transmissive conductive layer which is formed in a light reflective region formed surrounding a light transmissive region and a second pixel electrode formed of a light transmissive conductive layer which is formed on the light reflective region,

the second pixel electrode is positioned as a lower layer with respect to an insulation film, a hole is formed in the insulation film in a region corresponding to the light transmissive region so as to expose the second pixel electrode, the first pixel electrode is formed in the light reflective region of the insulation film,

a light shielding film which is positioned as a layer below the insulation film is provided to at least a portion corresponding to a side wall surface of the hole formed in the insulation film and,

the light shielding film is made of a material equal to a material of the gate signal lines and is formed as a layer below the second pixel electrode and, at the same time, there exists a portion where the light shielding layer is not formed at a part of the portion corresponding to the side wall surface of the hole formed in the insulation film and the portion includes a portion which is close to the switching element.